



Attorney's Docket No.: 10417-102001 / F51-138531M/SW

2823
1/28/04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Noriaki Sakamoto et al.

Art Unit : 2823

Serial No. : 09/970,013

Examiner : Brook Kabede

Filed : October 2, 2001

Title : METHOD OF MANUFACTURING CIRCUIT DEVICE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

REPLY TO ACTION OF OCTOBER 15, 2003

In reply to the Office Action of October 15, 2003, Applicant submits the following remarks.

REMARKS

Claims 1-27 are pending.

Claim Objections - Specification

The Applicants have submitted herewith an IDS of possibly related co-pending applications. If the Examiner is aware of any other co-pending art, please include them on a "Notice of References Cited."

Claim Rejections – Double Patenting

Claims 1-27 were rejected for obviousness-type double patenting over U.S. Pat. No. 6,531,370 to Sakamoto et al.

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